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## [1 Verdantium towards a Java-enabled compound document model \(poster session\)](#)


**Thornton Green**
**January 2000 [Addendum to the 2000 proceedings of the conference on Object-oriented programming, systems, languages, and applications \(Addendum\)](#)**

Full text available: [pdf\(22.80 KB\)](#) Additional Information: [full citation](#), [abstract](#), [index terms](#)

Java Beans provides a robust mechanism for combining software components. However, to a large extent Java Beans components are currently not being combined to create documents in an environment such as a word processor or illustration program. This is not due to a weakness in the Beans property model, but rather to limitations in how Java Beans' user interfaces are currently implemented. Verdantium is a prototype Java-based API (for JDK 1.2.2 and above) for document embedding that takes a nov ...

## [2 Numerically estimating internal models of dynamic virtual objects](#)


**G. Robles-De-La-Torre, R. Sekuler**
**October 2004 [ACM Transactions on Applied Perception \(TAP\)](#), Volume 1 Issue 2**

Full text available: [pdf\(584.66 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Precise manipulation of objects is ordinarily limited by visual, kinesthetic, motor, and cognitive factors. Specially designed virtual objects and tasks minimize such limitations, making it possible to isolate and estimate the internal model that guides subjects' performance. Subjects manipulated a computer-generated virtual object (*vO*), attempting to align *vO* to a target whose position changed randomly every 10 s. To analyze the control actions subjects use while manipulating the ...

**Keywords:** Dynamics, human cognition, human information processing, ideal performer, internal model, virtual object, virtual reality

**3 Session 3A: Sequential synthesis: Solution of parallel language equations for logic synthesis**

Nina Yevtushenko, Tiziano Villa, Robert K. Brayton, Alex Petrenko, Alberto L. Sangiovanni-Vincentelli

November 2001 **Proceedings of the 2001 IEEE/ACM international conference on Computer-aided design**

Full text available: [!\[\]\(a03a7eb2f4046e1d3c76772003e549ea\_img.jpg\) pdf\(166.23 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

The problem of designing a component that combined with a known part of a system, conforms to a given overall specification arises in several applications ranging from logic synthesis to the design of discrete controllers. We cast the problem as solving abstract equations over languages. Language equations can be defined with respect to several language composition operators such as synchronous composition,  $\bullet$ , and parallel composition,  $\diamond$ ; conformity can be checked by language containm ...

**4 Motion capture, editing & planning: Mapping optical motion capture data to skeletal motion using a physical model**

Victor B. Zordan, Nicholas C. Van Der Horst

July 2003 **Proceedings of the 2003 ACM SIGGRAPH/Eurographics Symposium on Computer animation**

Full text available: [!\[\]\(4fe57c3593bf1b21d272ae7ac8dfaf77\_img.jpg\) pdf\(5.39 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Motion capture has become a premiere technique for animation of humanlike characters. To facilitate its use, researchers have focused on the manipulation of data for retargeting, editing, combining, and reusing motion capture libraries. In many of these efforts joint angle plus root trajectories are used as input, although this format requires an inherent mapping from the raw data recorded by many popular motion capture set-ups. In this paper, we propose a novel solution to this mapping problem ...

**5 Posters: HAT: a hardware assisted TOP-DOC inverted index component**

S. Kagan Agun, Ophir Frieder

July 2003 **Proceedings of the 26th annual international ACM SIGIR conference on Research and development in informaion retrieval**

Full text available: [!\[\]\(84f47badaad7772cd95667a7c387a639\_img.jpg\) pdf\(158.87 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

A novel Hardware Assisted Top-Doc (HAT) component is disclosed. HAT is an optimized content indexing device based on a modified inverted index structure. HAT accommodates patterns of different lengths and supports a varied posting list versus term count feature sustaining high reusability and efficiency. The developed component can be used either as an internal slave component or as an external co-processor and is efficient in resource demands as the component controllers take only a minimal per ...

**Keywords:** hardware support, inverted index file

## **6 Automatic production of controller specifications from control and timing behavioral descriptions**

S. Hayati, A. Parker

June 1989 **Proceedings of the 26th ACM/IEEE conference on Design automation**

Full text available:  pdf(757.60 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper presents a method for the generation of controller specifications from high-level behavioral descriptions in control and timing graph form. Input descriptions may contain multiple timing constraints, asynchronous and synchronous inputs, data dependent internal loops, and parallel and conditional branches. The timing graph model is transformed automatically to a state table specification of a synchronous finite state machine. The specification method is effective not only for inde ...

## **7 High-performance multi-queue buffers for VLSI communications switches**

Y. Tamir, G. L. Frazier

May 1988 **ACM SIGARCH Computer Architecture News , Proceedings of the 15th Annual International Symposium on Computer architecture**, Volume 16 Issue 2

Full text available:  pdf(1.41 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Small nxn switches are key components of multistage interconnection networks used in multiprocessors as well as in the communication coprocessors used in multicomputers. The design of the internal buffers in these switches is of critical importance for achieving high throughput low latency communication. We discuss several buffer structures and compare them in terms of implementation complexity and their ability to deal with variations in traffic patterns a ...

## **8 Resource sharing in hierarchical synthesis**

Oliver Bringmann, Wolfgang Rosenstiel

November 1997 **Proceedings of the 1997 IEEE/ACM international conference on Computer-aided design**

Full text available:  pdf(80.14 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

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This paper presents a new approach to hierarchical high-level synthesis with respect to internal register-transfer structures of complex components. Entire subdesigns can efficiently be used as complex components at a higher hierarchical level of the design. After synthesis, the calculated schedule of each subdesign is added to its register-transfer component model. This enables the sharing of unused subcomponents across different hierarchical levels of the design. Especially, subcomponents of a ...

**Keywords:** High-Level Synthesis, Resource Sharing, Hierarchical Synthesis

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**1 A two degrees of freedom control design for robot manipulator using internal model structure***Chan, S.P.;*

Industrial Electronics, Control and Instrumentation, 1991. Proceedings. IECON '91., 1991 International Conference on, 28 Oct.-1 Nov. 1991

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Industrial Electronics, 1996. ISIE '96., Proceedings of the IEEE International Symposium on, Volume: 1, 17-20 June 1996

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Mechatronics, IEEE/ASME Transactions on, Volume: 9, Issue: 2, June 2004

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[\[Abstract\]](#) [\[PDF Full-Text \(632 KB\)\]](#) **IEEE JNL****4 Velocity ripple elimination of AC permanent motor by using internal model principle***Wai-Chuen Gan; Li Qiu;*

Power Electronics and Motion Control Conference, 2000. Proceedings. PIEMC 2000. The Third International, Volume: 1, 15-18 Aug. 2000

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